

L Number	Hits	Search Text	DB	Time stamp
1	144	(multi-bit adj3 symbol\$1) or (multiple-bit adj3 symbol\$1)	USPAT; EPO; JPO; DERWENT	2004/04/22 11:06
2	35	((multi-bit adj3 symbol\$1) or (multiple-bit adj3 symbol\$1)) and (ECC or (error adj2 correction adj2 code\$1))	USPAT; EPO; JPO; DERWENT	2004/04/22 11:07
4	109	((multi-bit adj3 symbol\$1) or (multiple-bit adj3 symbol\$1 or n-bit\$1)) near10 stor\$3) and (ECC or (error adj2 correction adj2 code\$1))	USPAT; EPO; JPO; DERWENT	2004/04/22 11:08
3	2	((multi-bit adj3 symbol\$1) or (multiple-bit adj3 symbol\$1)) near10 stor\$3) and (ECC or (error adj2 correction adj2 code\$1))	USPAT; EPO; JPO; DERWENT	2004/04/22 11:14
5	1	((multi-bit adj3 symbol\$1) or (multiple-bit adj3 symbol\$1)) and (solid adj2 state adj2 stor\$3) and (ECC or (error adj2 correction adj2 code\$1))	USPAT; EPO; JPO; DERWENT	2004/04/22 11:14
6	10	((multi-bit\$1) or (multiple-bit\$1)) and (solid adj2 state adj2 stor\$3) and (ECC or (error adj2 correction adj2 code\$1))	USPAT; EPO; JPO; DERWENT	2004/04/22 11:21
7	223	((multi-bit\$1) or (multiple-bit\$1)) and (solid adj2 state adj2 stor\$3) or (storage adj2 device) or (secondary adj2 memroy) or (mass adj2 storage)) and (ECC or (error adj2 correction adj2 code\$1))	USPAT; EPO; JPO; DERWENT	2004/04/22 11:22
8	3	((multi-bit\$1) or (multiple-bit\$1)) and (solid adj2 state adj2 stor\$3) or (storage adj2 device) or (secondary adj2 memroy) or (mass adj2 storage)) and (ECC adj2 encoder)	USPAT; EPO; JPO; DERWENT	2004/04/22 11:23
9	6	((multi-bit\$1) or (multiple-bit\$1)) and (solid adj2 state adj2 stor\$3) or (storage adj2 device) or (secondary adj2 memroy) or (mass adj2 storage)) and (ECC adj2 decoder)	USPAT; EPO; JPO; DERWENT	2004/04/22 11:24
10	9	((multi-bit\$1) or (multiple-bit\$1)) and (solid adj2 state adj2 stor\$3) or (storage adj2 device) or (secondary adj2 memroy) or (mass adj2 storage)) and (ECC near2 encod\$3)	USPAT; EPO; JPO; DERWENT	2004/04/22 11:24
11	30	(multi-bit\$1 or multiple-bit\$1 or eight-bit\$1 or 8-bits) and (solid adj2 state adj2 stor\$3) or (storage adj2 device) or (secondary adj2 memroy) or (mass adj2 storage)) and (ECC near2 encod\$3)	USPAT; EPO; JPO; DERWENT	2004/04/22 11:25
12	21	(multi-bit\$1 or multiple-bit\$1 or n-bit\$1) and ((solid adj2 state adj2 stor\$3) or (storage adj2 device) or (secondary adj2 memroy) or (mass adj2 storage)) and (ECC near2 encod\$3)	USPAT; EPO; JPO; DERWENT	2004/04/22 11:26
13	21	(multi-bit\$1 or multiple-bit\$1 or n-bit\$1) and ((solid adj2 state adj2 stor\$3) or (storage adj2 device) or (secondary adj2 memroy) or (mass adj2 storage)) and (ECC near2 encod\$3) and Reed-Solomon	USPAT; EPO; JPO; DERWENT	2004/04/22 11:26
14	14	(multi-bit\$1 or multiple-bit\$1 or n-bit\$1) and ((solid adj2 state adj2 stor\$3) or (storage adj2 device) or (secondary adj2 memroy) or (mass adj2 storage)) and (ECC near2 encod\$3) and Reed-Solomon and controller	USPAT; EPO; JPO; DERWENT	2004/04/22 11:26

15	19	(multi-bit\$1 or multiple-bit\$1 or n-bit\$1) and ((solid adj2 state adj2 stor\$3) or (storage adj2 device) or (secondary adj2 memroy) or (mass adj2 storage)) and (ECC near2 encod\$3) and Reed-Solomon and control\$4	USPAT; EPO; JPO; DERWENT	2004/04/22 11:27
16	0	(multi-bit\$1 or multiple-bit\$1 or n-bit\$1) and ((solid adj2 state adj2 stor\$3) or (storage adj2 device) or (secondary adj2 memroy) or (mass adj2 storage)) and (ECC near2 encod\$3) and ((single adj2 row) or (one adj2 row))	USPAT; EPO; JPO; DERWENT	2004/04/22 11:28
17	21	((solid adj2 state adj2 stor\$3) or (storage adj2 device) or (secondary adj2 memroy) or (mass adj2 storage)) and (ECC near2 encod\$3) and ((single adj2 row) or (one adj2 row))	USPAT; EPO; JPO; DERWENT	2004/04/22 11:28